

An Electronic Circuit System for Time-Reversal of Ultra-Wideband Short Impulses Based on Frequency-Domain Approach

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Abstract—In this paper, a compact and low-cost electronic circuit system is designed for time-reversal of ultra-wideband short impulses (with nanosecond and sub-nanosecond temporal durations). A frequency-domain approach is adopted to avoid high sampling rate in time. Specifically, the proposed system obtains the discrete spectra of input impulses first; then realizes time-reversal in frequency domain; and finally synthesizes the time-reversed impulses using discrete continuous wave elements. This system is composed of common and commercially available circuits, and hence, can embody a system-on-chip implementation. Its performance is verified by circuit-electromagnetic co-simulations using impulses with 3–10-GHz frequency band coverage. Advanced Design System and two full-wave Maxwell’s equations solvers are used for circuit and electromagnetic simulations, respectively, and their results are coupled and integrated. In the circuit part, most of nonidealities of realistic circuits are taken into account. It is shown by the simulation results that, although realistic circuits unavoidably introduce errors to time-reversal, such errors do not affect the “focusing” phenomena in the context of electromagnetic wave propagation. As a conclusion, the proposed system can be deployed in practical time-reversal communication and radar applications.

Index Terms—Electronic circuit system, frequency-domain approach, short impulses, time reversal, ultra-wideband (UWB).

I. INTRODUCTION

DUE TO its capability of focusing waves in both space and time, time-reversal technique has attracted enormous interests from engineers, as well as physicists [1]–[3]. Specifically, time-reversal takes advantage of channel reciprocity to achieve “focusing,” which typically involves three steps. First, signals propagate through *forward channel*; second, the signals after passing the forward channel are reversed in time; and finally, the time-reversed signals are transmitted through *reverse channel*. Channel information is collected through the forward trip. If the forward and reverse channels share the same transfer functions (i.e., reciprocal channel), the channel is compensated during the reverse trip because time-reversal behaves as a space-

time matched filter (time-reversal is equivalent to complex conjugate in frequency domain) [4], [5].

Time-reversal can be applied to both sound waves and electromagnetic waves. In acoustics, time-reversal has been studied extensively [6]–[9]. As a far from complete list, it was successfully implemented for underwater communication [8], [10] and target detection/imaging [11]–[13]. The studies of time-reversal in electromagnetics also have a long history; especially in recent years, a lot of research work on time-reversal for electromagnetic waves has been reported [14]–[24], which is largely driven by the advance of impulse-type ultra-wideband (UWB) technology [25]–[27]. It is demonstrated by many researchers that the combination of time-reversal and UWB makes it possible to develop various novel communication and radar systems [28]–[35] including, but not limited to, multiple-input multiple-output (MIMO) communication [36], indoor communication [37], [38], communications in forest environments [39], buried target detection [24], ground penetrating radar [22], [40], and breast cancer diagnosis [41], [42]. Indeed, the space-time focalization capability associated with time-reversal seems to have unbounded applications: innovative methods and systems based upon time-reversal keep being proposed by researchers in recent years. For instance, time-reversal enables sophisticated “compressed sensing” in radar detection, which allows far fewer measurements than traditional sensing technologies [43]. As another example, since time-reversal can effectively conquer highly dispersive channels, it has potential to realize high-speed wireless communication in complex environments like metallic boxes [44].

Relatively speaking, time-reversal is not so mature a technique in electromagnetics as in acoustics. Although numerous time-reversal UWB communication and radar systems have been proposed, their practical implementations remain challenging tasks. One of the critical difficulties is how to efficiently realize time-reversal of UWB impulses. Compared to sound waves, electromagnetic waves have high operating frequencies (for instance, 3.1–10.6-GHz band was allocated for UWB communication applications by the Federal Communications Commission). As a result, much faster sampling is required to directly sample the impulses and then reverse them. To experimentally investigate time-reversal of electromagnetic waves, some researchers had to resort to expensive high-speed analog-to-digital converters [45]–[52]. As an example, Tektronix TDS6604 digital storage oscilloscope with 20-GSamps/s was used in [46]. In some research efforts, time-reversal

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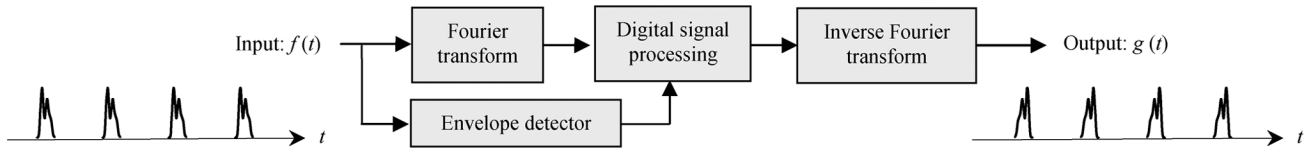


Fig. 1. Block diagram of the proposed time-reversal system.

was realized using vector network analyzers, where data were collected in frequency domain initially and synthesized afterwards [53]–[58]. Microwave signal with 18-GHz bandwidth was time reversed in [59] with aids of up-conversion to optical carrier and nonlinear optical complex conjugate. Obviously, to accomplish practical time-reversal communication and radar systems, efficient and low-cost techniques to reverse UWB impulses are called for. In this paper, an electronic circuit system is designed to time reverse UWB impulses (with nanosecond and sub-nanosecond temporal durations). A frequency-domain approach is adopted to avoid high sampling rate in the time domain. Specifically, time-reversal is achieved in the following three stages.

- 1) Fourier transform, to obtain discrete spectra of input impulses.
- 2) Digital signal processing in frequency domain, to carry out complex conjugate of the spectra obtained in 1).
- 3) Inverse Fourier transform, to synthesize reversed impulses based on the processed spectra in 2).

The proposed system is composed of common semiconductor electronic circuits, including oscillators, multipliers/mixers, bandpass filters (BPFs), amplifiers, phase shifters, and switches. Thus, it is compact and low cost; moreover, it can embody a system-on-chip implementation. It is worth noting that, the authors are not the first ones who attempt applying frequency-domain approaches to UWB signal generation and reception. Sub-band analysis [60], [61] and bank of passive resonators [62] were investigated before to relieve the high sampling rate problem in UWB receivers; and, Fourier synthesis was suggested for UWB signal generators as early as in 1997 [63]. However, to the best of the authors' knowledge, the hardware architecture in this paper, which combines Fourier transform and inverse Fourier transform circuits to accomplish time-reversal of UWB signals, had never been reported. Basic concept of the system proposed in this paper was presented by the authors in [64]; here, complete description and verification are provided. The proposed circuit system is simulated by the Advanced Design System (ADS) with commercially available components; most of the nonidealities of realistic circuits are taken into account. Simulation results using impulses with around 1-ns temporal width and [3], [10] gigahertz spectral coverage demonstrate real-time and reliable time-reversal, even with the presence of strong noise. Moreover, performance of the proposed time-reversal circuit system is verified in the context of electromagnetic propagation. Circuit-electromagnetic co-simulations are carried out for two real-world problems (one is for wireless communication and the other is for radar detection). Two full-wave solvers, finite difference time domain (FDTD) [65] and method of moments (MoM) [66], are used to solve the Maxwell's equations; and they are coupled with the circuit simulator. Electromagnetic propagation results with

ideal time-reversal and practical time-reversal (i.e., time-reversal using circuits in this paper) are compared. In both cases, desired "focusing" phenomena are observed. It is, therefore, concluded that, errors introduced by the time-reversal circuits are tolerable and the proposed system can be deployed in practical communication and radar applications.

This paper is organized as follows. In Section II, the proposed time-reversal circuit system is described in detail. Section III presents results from both circuit simulation and circuit-electromagnetic co-simulation, to demonstrate the performance of the system in Section II. Finally, conclusions of this study are drawn in Section IV.

II. DESCRIPTION OF THE PROPOSED TIME-REVERSAL SYSTEM

The proposed time-reversal circuit system is described in this section. Section II-A presents the system design. Three major blocks of the system, which include Fourier transform, inverse Fourier transform, and digital signal processing, are discussed in Sections II-B–D, respectively.

A. System Design

Block diagram of the proposed time-reversal system is depicted in Fig. 1. The input is a periodic signal consisting of a series of short impulses (with nanosecond or sub-nanosecond temporal durations). The time-reversal system has three major blocks.

- 1) Fourier transform block obtains discrete spectrum of the input signal.
- 2) Digital signal processing block processes the spectrum obtained in 1).
- 3) Inverse Fourier transform block makes use of spectrum in 2) to synthesize the output, which is a periodic signal with each impulse in the input signal reversed.

In addition to the three major blocks above, envelope detector block acquires the positions of impulses in the input signal to facilitate signal processing.

It is assumed that the input signal $f(t)$ has period T_0 and there is a short impulse within each period, as illustrated in Fig. 2. The short impulse within interval $t \in [0, T_0]$ has starting time t_a and ending time t_b . Since $f(t)$ is periodic, it can be represented by Fourier series as

$$f(t) = \sum_{m=-\infty}^{+\infty} c_m e^{jm\omega_0 t} \quad (1)$$

where $j = \sqrt{-1}$, $\omega_0 = 2\pi/T_0$, and

$$c_m = \frac{1}{T_0} \int_0^{T_0} f(t) e^{-jm\omega_0 t} dt. \quad (2)$$

Also, it is assumed that the input signal's discrete spectrum is virtually limited within minimum frequency ω_{\min} and max-

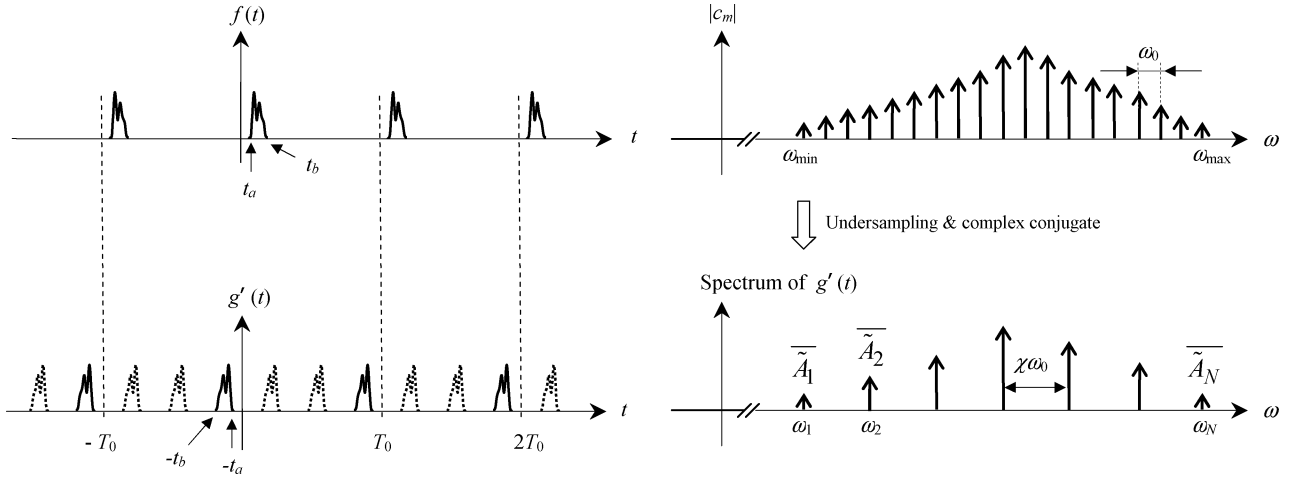


Fig. 2. Illustration of signals in the proposed time-reversal system.

imum frequency ω_{\max} . That is, $|c_m| \approx 0$ when $|m\omega_0| > \omega_{\max}$ or $|m\omega_0| < \omega_{\min}$ (Fig. 2).

In the Fourier transform block of the proposed system, a subset of $\{c_m\}$ is selected. The elements of this subset are denoted $\{\tilde{A}_1, \tilde{A}_2, \dots, \tilde{A}_N\}$, and they are Fourier series of $f(t)$ located at $\{\omega_1, \omega_2, \dots, \omega_N\}$, where $\omega_n = \omega_1 + (n-1)\chi\omega_0$, $n = 1, 2, \dots, N$

$$N = \left\lceil \frac{\omega_{\max} - \omega_{\min}}{\chi\omega_0} \right\rceil + 1 \quad (3)$$

$\chi \geq 1$ is termed the undersampling ratio and is chosen as

$$\chi = \left\lceil \frac{2\pi}{\omega_0(t_b - t_a)} \right\rceil \quad (4)$$

and the two operators $\lfloor \cdot \rfloor$ and $\lceil \cdot \rceil$ find the nearest integers smaller and greater than arguments respectively. Next, complex conjugate is carried out onto the selected spectral samples $\{\tilde{A}_n\}$, in the digital signal processing block. Finally in the inverse Fourier transform block, the processed spectral samples are used to construct $g'(t)$

$$g'(t) = 2\text{Re} \left\{ \sum_{n=1}^N \overline{\tilde{A}_n} e^{j\omega_n t} \right\} \quad (5)$$

where $\overline{\tilde{A}_n}$ is the complex conjugate of \tilde{A}_n . Due to undersampling, temporal period of $g'(t)$ is T_0/χ . Desired output $g(t)$ can be obtained from $g'(t)$ by removing unnecessary impulses. (In Fig. 2, $g'(t)$ is illustrated with $\chi = 3$ and unnecessary impulses are denoted by dashed lines.) The envelope detector block assists determining which impulses to remove.

Detailed block diagram of the proposed system is given in Fig. 3. Among the four blocks, envelope detector block is the simplest. It follows a standard amplitude de-modulator design, where $\omega_c = (\omega_{\min} + \omega_{\max})/2$. The output of low-pass-filter in the envelope detector block of Fig. 3 contains narrow impulses and the rough locations of these impulses are measured to be t_k^* and $t_k^* \in [kT_0 + t_a, kT_0 + t_b]$ for $k = \dots, -1, 0, 1, 2, \dots$. The other three blocks, Fourier transform, inverse Fourier transform, and digital signal processing, are discussed in Sections II-B-D, respectively.

B. Fourier Transform Block

The Fourier transform block makes use of N local oscillators to find N spectral samples of the input signal. Specifically, the frequencies of local oscillators are $\omega_n - \omega_0/4$, $n = 1, 2, \dots, N$. The mixers followed by oscillators behave as down-converters. After down-conversion by the n th oscillator, the spectrum of the input signal is depicted in Fig. 4. Note that $f(t)$'s spectral sample at ω_n is moved to location $\omega = \omega_0/4$ after down-conversion; and its sample at $-\omega_n$ is relocated to $\omega = -\omega_0/4$. BPFs centered at frequency $\omega_0/4$ and with sufficiently small bandwidth filter out spectral sample \tilde{A}_n . In Fig. 4, the BPF's frequency response is denoted by two impulses with dashed lines. Since the output of BPF is a low-frequency signal (i.e., oscillating at frequency $\omega_0/4$), its magnitude and phase can be easily detected and are denoted as complex phasor A_n . Apparently

$$A_n = \tilde{A}_n e^{-j\phi_n} \quad (6)$$

where ϕ_n is the phase of the n th local oscillator. The above architecture is superheterodyne, where an intermediate frequency ($\omega_0/4$) is processed after down-conversion. It is noted that superheterodyne processing is not the only option to analyze the input signal's spectrum. In [62], passive resonators are used to catch the spectral lines directly. In [64], analog correlator configuration ("multiplier + integrator") was exploited to realize the Fourier transform. The superheterodyne architecture is adopted here, because it offers two unique advantages. First, down-conversion avoids processing the input signal at high frequencies; in contrast, resonators with high quality factors must be built at gigahertz range with accurate resonant frequencies in [62]. Second, processing at intermediate frequency is much easier than at dc; for instance, the analog correlator design in [64] is sensitive to the isolation ratio in mixers while this issue is suppressed by the BPFs in the scheme in Fig. 3.

A few accessory circuits are needed to secure desirable operation of the Fourier transform block; and they are explained in the remainder of this section.

Frequencies of the N local oscillators must be locked to preset values. Instead of regular phase-locked loops, a simple locking circuit is proposed for the system in this paper. As

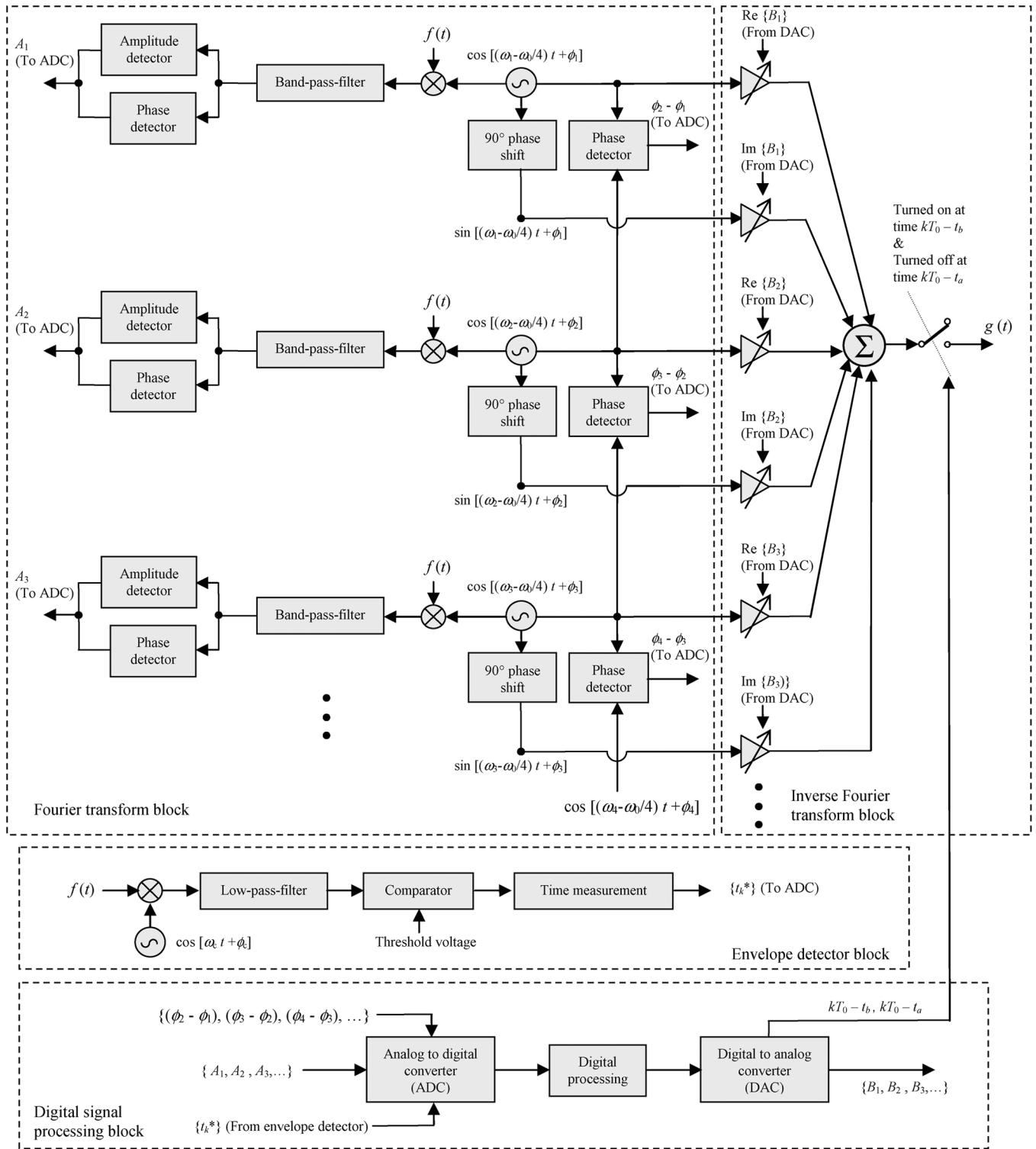


Fig. 3. Detailed schematic diagram of the proposed time-reversal system.

shown in Fig. 5, the n th oscillator's frequency is controlled by a bias signal as $\omega_n - \omega_0/4 + \Delta\omega$. This oscillator is mixed with the input signal $f(t)$; and the mixer's output goes to a BPF centered at $\omega_0/4$ and with a high quality factor (high Q). Since the input signal is periodic, the output of BPF is maximized when $\Delta\omega = 0$. Obviously, the narrower the BPF's bandwidth is, the smaller phase error there is. However, if the BPF has too

high a quality factor, a lot of time is needed for it to reach steady state, and consequently, the locking speed is low. In practice, a medium bandwidth should be chosen such that BPFs in Fig. 3 could capture desired spectral lines with sufficient accuracy.

Phase of the first local oscillator, ϕ_1 , is assumed to be zero; and phases of the other oscillators are obtained through phase detectors in Fig. 3. The block diagram of phase detector be-

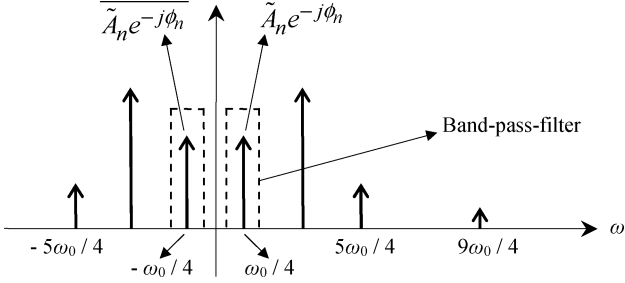
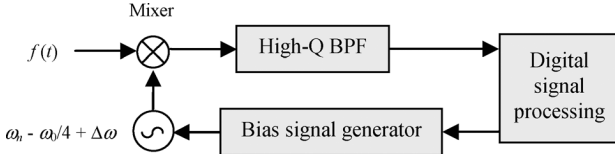
Fig. 4. Illustration of spectrum after down-conversion with the n th oscillator.

Fig. 5. Block diagram of the frequency locking circuit.

tween oscillators at $\omega_{n-1} - \omega_0/4$ and $\omega_n - \omega_0/4$ is shown in Fig. 6. The two continuous-wave signals are multiplied and only the difference frequency component survives after the BPF. It is noted that, the output after BPF is oscillating at frequency $\omega_n - \omega_{n-1} = \chi\omega_0$. When this signal is compared with an appropriately chosen threshold voltage, periodic pulses are resulted, as shown in Fig. 6. Apparently, the edges' timings in the comparator's output are directly related to phase difference $\phi_n - \phi_{n-1}$

$$\phi_n - \phi_{n-1} = -\phi_{\text{BPF}} - \frac{t_1 + t_2}{2}(\omega_n - \omega_{n-1}) \quad (7)$$

where ϕ_{BPF} is the phase response of the BPF in Fig. 6 at frequency $\omega_n - \omega_{n-1}$. Composition of the time measurement block in Fig. 6 is plotted in Fig. 7. The rise/fall edge samples a ramp signal and a digital counter. The ramp signal has period T_0/χ . When the ramp signal is sampled, its output voltage is linearly proportional to a time between 0 and T_0/χ . At the same time, the clock shares the same period T_0/χ and the counter counts the number of T_0/χ that have passed. Both the ramp signal and clock are reset every T_0 . Consequently, the samplings of ramp signal and digital counter jointly measure the timing in the range of $[0, T_0]$.

In addition, isolators/buffers must be put between the input signal $f(t)$ and every mixer to avoid couplings among the multiple local oscillators.

C. Inverse Fourier Transform Block

The inverse Fourier transform block exploits the same set of oscillators as those in the Fourier transform block. The adder circuit in Fig. 8 combines the N oscillators' outputs. Magnitudes and phases of the N continuous-wave elements are adjusted by $2N$ variable gain amplifiers (Fig. 3). As a result, the synthesized signal after adder is

$$g'(t) = \frac{R_F}{R} \text{Re} \left\{ \sum_{n=1}^N B_n e^{j((\omega_n - \frac{\omega_0}{4})t + \phi_n)} \right\} \quad (8)$$

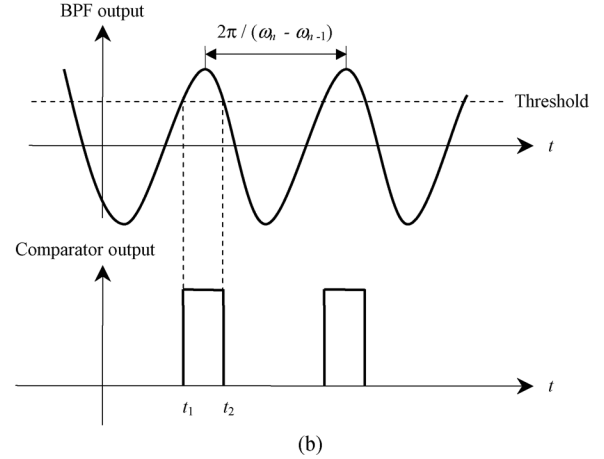
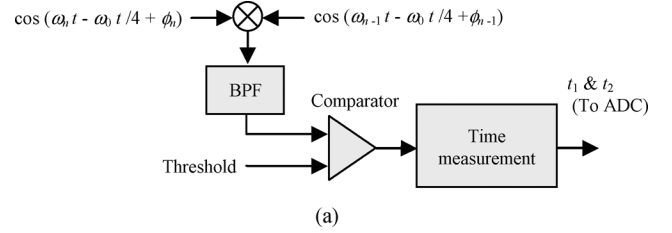


Fig. 6. Block diagram of phase detector circuit. (a) Circuit block diagram. (b) Illustrations of outputs of BPF and comparator.

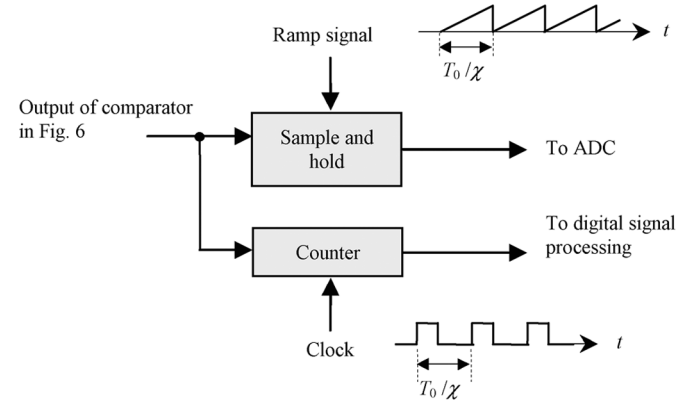


Fig. 7. Block diagram of time measurement circuit.

where R_F and R are the two resistances in Fig. 8. The desired output $g(t)$ is obtained by multiplying g' by windowing function $W(t)$

$$g(t) = g'(t)W(t) \quad (9)$$

and

$$W(t) = \begin{cases} 1, & t \in [kT_0 - t_b, kT_0 - t_a] \\ 0, & \text{elsewhere} \end{cases}, \quad k = \dots, -1, 0, 1, 2, \dots \quad (10)$$

Physically, multiplication with the windowing function is realized by a switch after the adder.

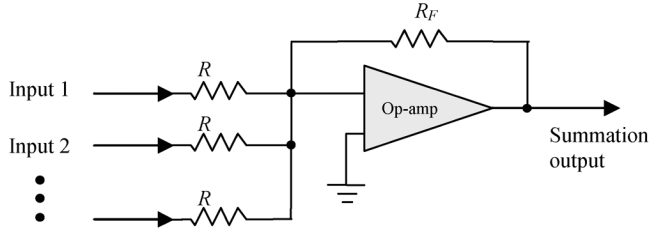


Fig. 8. Block diagram of adder in inverse Fourier transform block.

D. Digital Signal Processing Block

The digital signal processing block has two major functionalities, which are: 1) to obtain coefficients $\{B_n\}$ and 2) to find switching timings $kT_0 - t_b$ and $kT_0 - t_a, k = \dots, -1, 0, 1, 2, \dots$

Comparison among (5), (6), and (8) yields that, for the inverse Fourier transform block to generate desired $g'(t)$ through (8), coefficients $\{B_n\}$ should be chosen as

$$B_n = \overline{A_n} e^{-j2\phi_n} e^{j\frac{\omega_0}{4}[(2k+1)T_0 - t_k^*]}$$

when

$$t \in [kT_0, (k+1)T_0]. \quad (11)$$

Equation (11) relies on the fact that $W(t)$ is a very narrow window around time $(2k+1)T_0 - t_k^*$; hence, $\exp\{j\omega_0 t/4\} \cong \exp\{j\omega_0[(2k+1)T_0 - t_k^*]/4\}$ when t is close to $(2k+1)T_0 - t_k^*$.

To find t_a and t_b , time-domain signal $\hat{f}(t)$ is reconstructed in the digital signal processing block using $\{A_n\}$ obtained by the Fourier transform block

$$\begin{aligned} \hat{f}(t) &= 2\text{Re} \left\{ \sum_{n=1}^N A_n e^{j(\omega_n t + \phi_n)} \right\} \\ &= 2\text{Re} \left\{ \sum_{n=1}^N \tilde{A}_n e^{j\omega_n t} \right\}. \end{aligned} \quad (12)$$

Obviously, signal $\hat{f}(t)$ is the ‘‘undersampled version’’ of input signal $f(t)$. It has period T_0/χ . In other words, there are χ impulses within one T_0 (the period of the input signal). The relationship between $f(t)$ and $\hat{f}(t)$ is illustrated in Fig. 9, when $\chi = 3$. To identify which one of the χ impulses coincides with the input signal, the following Fourier transform is performed:

$$\begin{aligned} \hat{F}_m &= \frac{\chi}{T_0} \int_{t_0}^{t_0 + T_0/\chi} \hat{f}(t) e^{-jm\omega_0 t} dt, \\ m &= \dots, -1, 0, 1, 2, \dots \end{aligned} \quad (13)$$

In the above, $t_0 \in [t_0^* - T_0/\chi, t_0^*]$ is a variable, and t_0^* is the rough location of the impulse of $f(t)$ in the range $t \in [0, T_0]$. The value of t_0 that minimizes the relative error between $\{\hat{F}_m\}$ and $\{c_m\}$ is picked as t_a , and $t_b = t_a + T_0/\chi$. In this paper, $\{c_m\}$

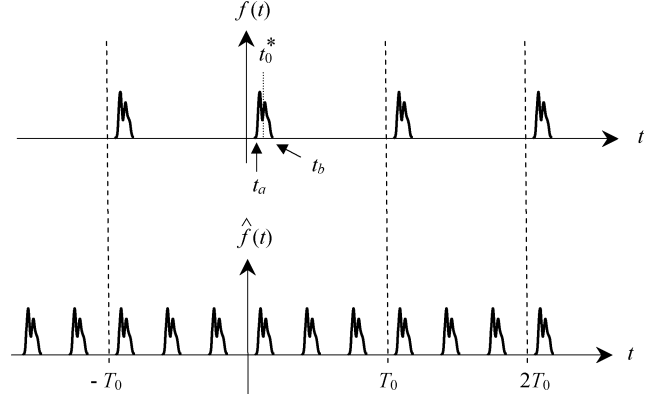

 Fig. 9. Illustration of algorithm to find t_a and t_b .

TABLE I
PARAMETERS OF KEY COMPONENTS USED IN ADS SIMULATION

Component	Company	Model	Parameters
Op-amp	Texas Instruments	THS4302	Gain-bandwidth product: 12 GHz
			Slew rate: 5500 V/us
			Gain: 14 dB
Local oscillator	Hittite	HMC587LC4B HMC388LP4 HMC506LP4	Frequency range: 3.15 – 10 GHz
			Power output: 5 dBm
			Phase noise: -95 dBc/Hz @100 kHz
			2nd harmonic: -15 dBc
Mixer	Teledyne Cougar	MMP12241	Bandwidth: 2 – 13 GHz
			IP3: 10 dBm
			Conversion loss: 5 dB
			LO-RF isolation ratio: 35 dB
Band-pass filter	AMCrf	AM21.4CR167	Noise figure: 8.5 dB
			Center frequency: 21.4 MHz
			Bandwidth: ± 10 kHz
			Insertion loss: 2.5 dB
Variable gain amplifier	Hittite	HMC625LP5 HMC694LP4	Pass-band ripple: 1 dB
			Bandwidth: DC – 17 GHz
			Tuning range of gain: 31.5 dB
			Gain flatness: ± 1 dB
Phase shifter	Harley	7724A 7722A	Frequency range: 2 – 12 GHz
			Control type: 10-bit digital
			Resolution: 1.41 degrees
			2nd harmonic: -30 dBc

are obtained by linear interpolation among $\{|A_n|\}$, which are results of the Fourier transform block.

III. SIMULATION RESULTS

In this section, the time-reversal circuit system proposed in Section II is verified by circuit simulation and circuit-electromagnetic co-simulation. Results from circuit simulation and circuit-electromagnetic co-simulation are presented in Sections III-A and B, respectively.

A. Circuit Simulation Results

The system in Fig. 3 is simulated by ADS. Commercial products with practical parameters are used for all the major components including local oscillators, mixers, variable gain amplifiers, phase shifters, BPFs, and op-amps. Model numbers and key parameters of these components are listed in Table I. Most of the nonidealities of realistic circuits are taken into account. For instance, the mixers have 35-dB isolation in between the two input ports; the variable gain amplifiers have gain range about 30 dB and phase response fluctuation $\pm 30^\circ$; and gain-bandwidth product of the op-amp is 12 GHz.

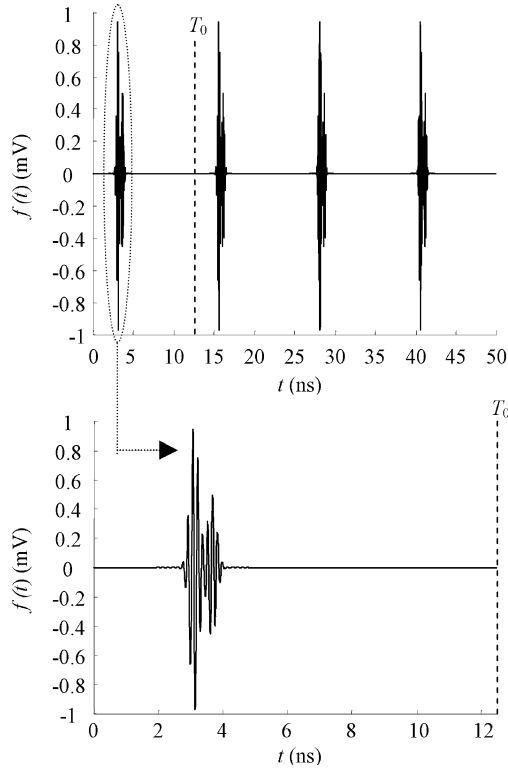


Fig. 10. Input signal to the time-reversal circuit (without noise).

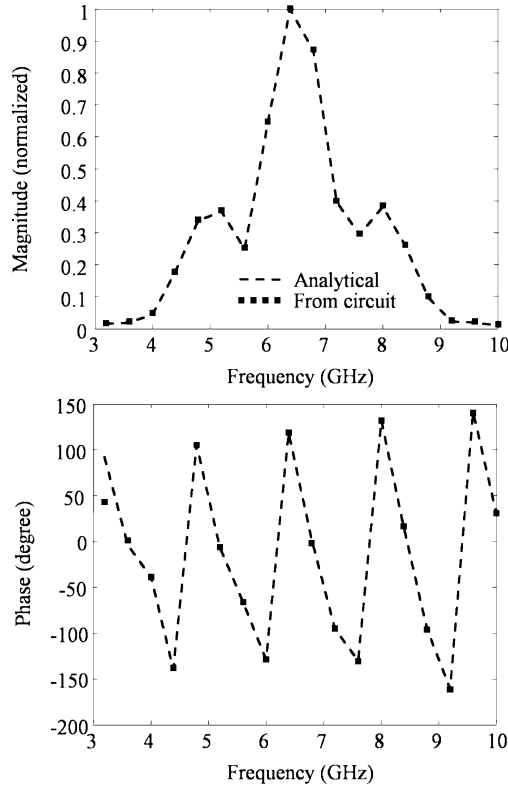


Fig. 11. Output of Fourier transform block (without noise).

In the simulation results of this section, the input signal to the time-reversal system is periodic with $T_0 = 12.5$ ns and

$$f(t) = \left[e^{-\frac{(t-t_\alpha)^2}{2\sigma^2}} + \frac{1}{2} e^{-\frac{(t-t_\beta)^2}{2\sigma^2}} \right] \cos(\omega_c t), \quad t \in [0, T_0] \quad (14)$$

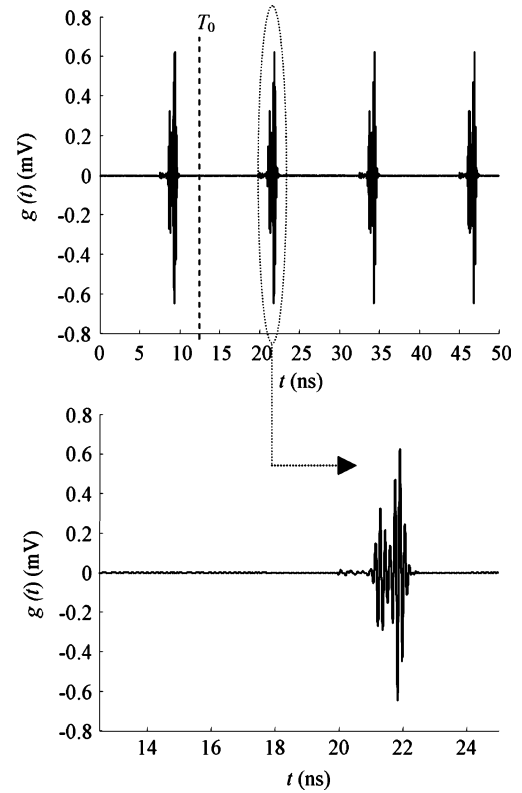


Fig. 12. Output signal of the time-reversal circuit (without noise).

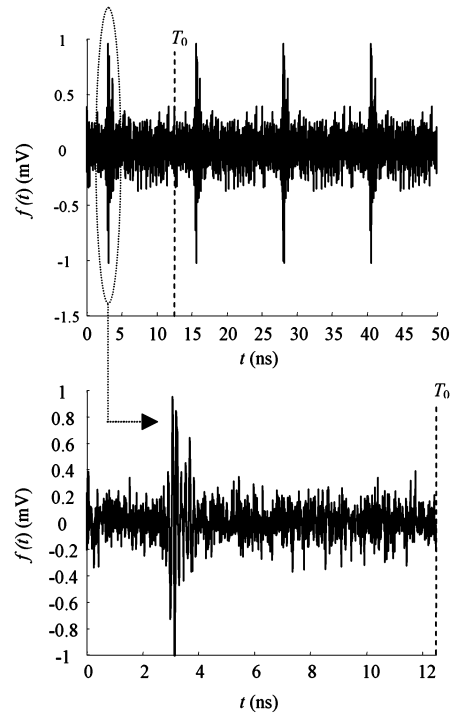


Fig. 13. Input signal to the time-reversal circuit (with noise).

where $\sigma = 138 \times 10^{-12}$ s, $t_\alpha = 3.125 \times 10^{-9}$ s, $t_\beta = t_\alpha + 4\sigma$, and $\omega_c = 2\pi \times 6.5 \times 10^9$ rad/s. Input signal in (14) is plotted in Fig. 10: it consists of two modulated Gaussian pulses, with total temporal duration about 1.4 ns and spectral coverage approximately from 3 to 10 GHz. In all the results in this section,

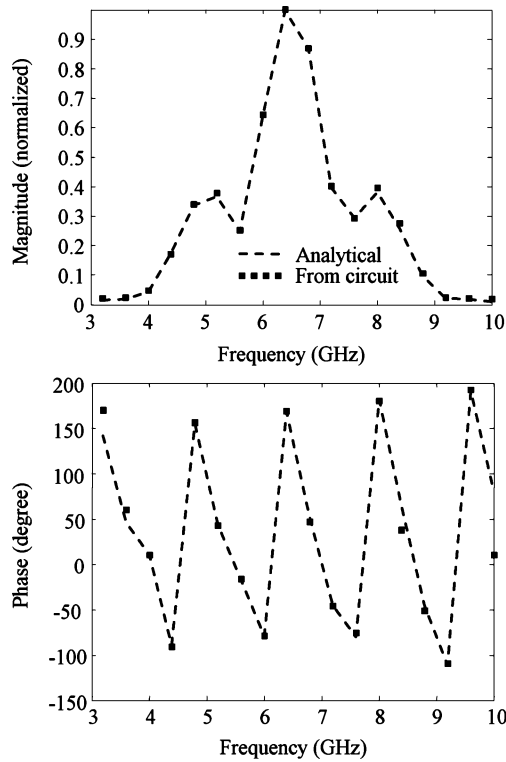


Fig. 14. Output of Fourier transform block (with noise).

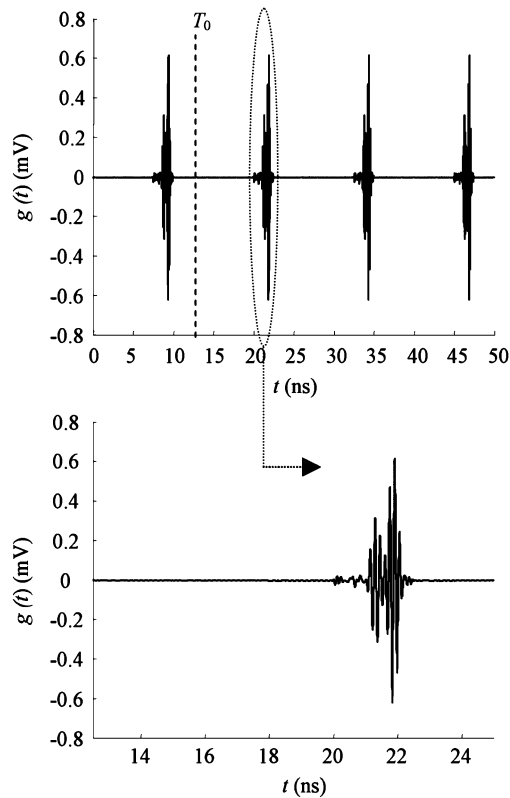


Fig. 15. Output signal of the time-reversal circuit (with noise).

$\chi = 5$, $N = 18$, and $\omega_1 = 2\pi \times 3.2 \times 10^9$ rad/s. The output of Fourier transform block is shown in Fig. 11 and has excellent agreement with analytical spectrum of (14). The waveform

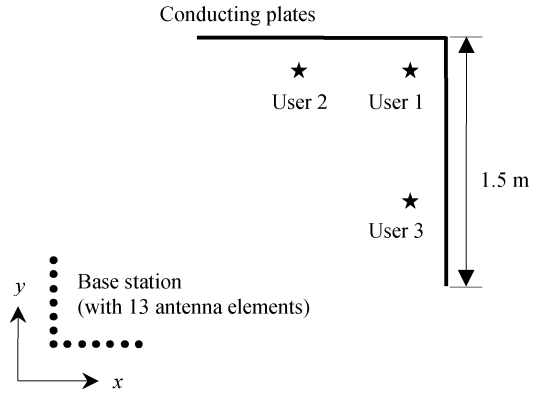


Fig. 16. Geometry of the wireless communication example.

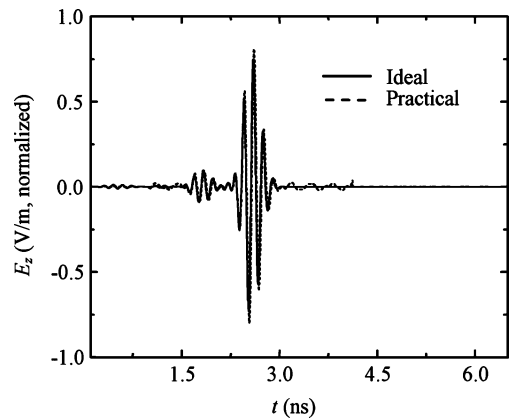


Fig. 17. Comparison of ideal and practical time-reversal at one of the antenna elements in the communication example.

after inverse Fourier transform block, $g(t)$, is plotted in Fig. 12. It is observed that, the proposed system successfully achieves time-reversal of the input signal. Relative to the Fourier transform block, the inverse Fourier transform block has more error sources, most of which are from the phase fluctuations of variable gain amplifiers and adder. From our experience, when the phase fluctuation is within $\pm 40^\circ$ degrees range, the ultimate time-reversed waveforms are usually acceptable.

The proposed time-reversal system is also tested in noisy scenarios. In Fig. 13, the input signal is plotted when a pretty strong additive white Gaussian noise is added to it. The signal-to-noise ratio is about 0 dB. When the signal in Fig. 13 is input into the system, outputs of Fourier transform block and inverse Fourier transform block are shown in Figs. 14 and 15, respectively. With the presence of strong noise, the proposed system reliably fulfills the time-reversal job. As a matter of fact, since the input signal is periodic in time and noise has continuous spectrum, signal-to-noise ratio after the Fourier transform block is much better than 0 dB. To a large extent, it is due to the narrow bandwidth of BPFs in Fig. 3, which is chosen as 300 kHz in this section.

B. Circuit-Electromagnetic Co-Simulation Results

In this section, the proposed time-reversal system is evaluated in the context of electromagnetic wave propagation. Realistic

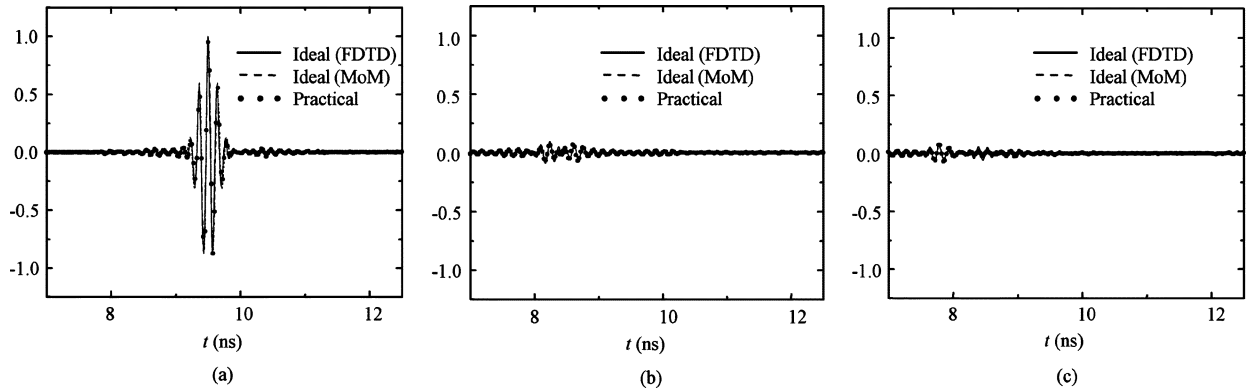


Fig. 18. Signals received at the three users when there is a link between the base station and User 1. (The vertical axes are normalized E_z .) (a) At User 1. (b) At User 2. (c) At User 3.

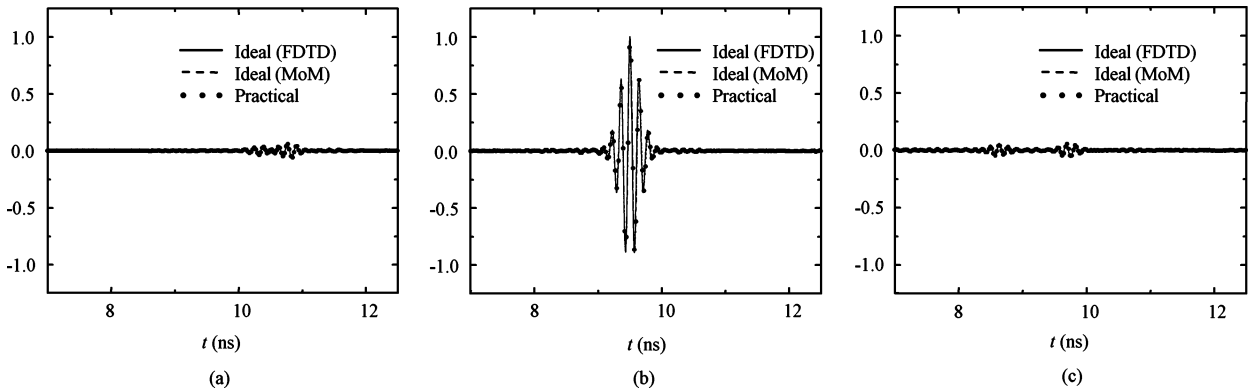


Fig. 19. Signals received at the three users when there is a link between the base station and User 2. (The vertical axes are normalized E_z .) (a) At User 1. (b) At User 2. (c) At User 3.

time-reversal communication and radar problem configurations are studied, which involves wave propagation, signal reception, and time-reversal of signals. The electromagnetic waves' propagation and reception are simulated by two Maxwell's equations solvers: one is based on the FDTD method [65], and the other on the MoM [66]. The FDTD solver obtains solutions in time domain; while the MoM solver is carried out in frequency domain and its results are inverse Fourier transformed to time domain afterwards. It is noted that both the FDTD and MoM solvers are full-wave solvers: they incorporate all the electromagnetic coupling and interaction phenomena. In this section, the electromagnetic solvers are always executed for the following two cases.

- 1) Ideal time-reversal. In this case, the FDTD and MoM solvers are independent of the circuits in Section II. Time-reversal of signals is carried out internal to the electromagnetic solvers, i.e., ideally.
- 2) Practical time-reversal. In this case, the FDTD and MoM solvers are coupled with the circuit simulator in Section III-A. To be specific, the electromagnetic solvers provide input signals to the ADS simulator and receive time-reversed signals from the circuit solver. In all the results in this section, $T_0 = 12.5$ ns, $\chi = 5$, $N = 18$, $\omega_1 = 2\pi \times 3.2 \times 10^9$ rad/s, and there is no noise present.

Two specific problem configurations are studied in this section. They are to simulate wireless communication and radar detection applications, respectively.

The wireless communication example is depicted in Fig. 16. Communication occurs in between a base station and three users. The base station is composed of 13 antenna elements; and the elements are geometrically arranged as in Fig. 16. There is one antenna at each user. All the antennas are assumed to be electrically small and behave as z -direction-oriented Hertzian dipoles with omni-directional radiation pattern in the x - y -plane. The three users are located at (1.65 m, 1.65 m, 0), (1.125 m, 1.65 m, 0), and (1.65 m, 0.95 m, 0), respectively. The antenna element at the corner in the base station has coordinates (0.15 m, 0.15 m, 0); and the distance among elements is 0.06 m. A corner reflector made of two perfectly conducting plates is placed around the three users to make the problem configuration complicated. The two plates both have length 1.5 m and height 0.01 m. Tip of the corner reflector sits at (1.8 m, 1.8 m, 0). It is not an easy task to achieve space division multiple access in the environment in Fig. 16. Suppose a regular phased array is implemented at the base station. Even if the phased array is able to deliver a narrow beam to a specific user, that beam would be bounced by the conducting plates and reach other users. The intention of this example is to demonstrate that space division multiple access could be accomplished through time-reversal together with the circuit system proposed in Section II. To establish a wireless communication link between User 1 and base station, User 1 transmits periodic short impulse by a current source excitation

$$I_z(t) = e^{-\frac{(t-t_0)^2}{2\sigma^2}} \cos(\omega_c t) \text{ (A)}, \quad \text{when } t \in [0, T_0] \quad (15)$$

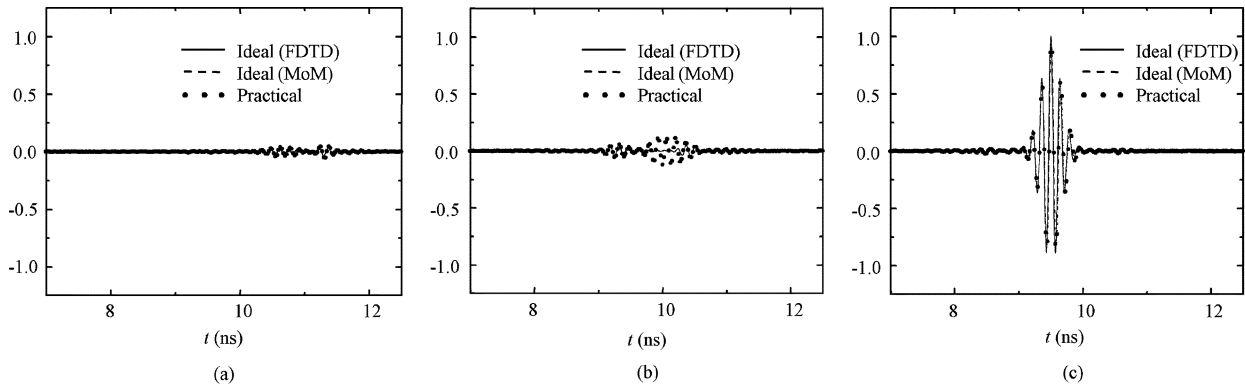


Fig. 20. Signals received at the three users when there is a link between the base station and User 3. (The vertical axes are normalized E_z .) (a) At User 1. (b) At User 2. (c) At User 3.

where $\sigma = 138 \times 10^{-12}$ s, $t_{\alpha} = 0.9996 \times 10^{-9}$ s, and $\omega_c = 2\pi \times 6.5 \times 10^9$ rad/s. (The signal in (15) virtually has spectral coverage of 3–10 GHz.) The fields radiated by User 1 are collected by all the 13 elements in the base station. Next, all the 13 elements time reverse their received signals and re-radiate them. Signals after ideal time-reversal and practical time-reversal are compared with one another in Fig. 17, where only one impulse out of the impulse train at the corner antenna element is plotted. Basically, ideal time-reversal and practical time-reversal match each other. As will be shown next, the small difference between them is not significant in the context of this communication example. When the radiations from the 13 elements of base station arrive at the three users, they are constructive at User 1, but destructive at the other two users. The signals received at the three users (electrical field along the z -direction and normalized by the maximum strength among the three users) are plotted in Fig. 18. Three sets of data are compared:

- 1) Results from FDTD solver with ideal time-reversal.
- 2) Results from MoM solver with ideal time-reversal.
- 3) Results from FDTD solver with practical time-reversal.

The three sets of data have nice agreement, and they all show that the signal is strong at User 1, but weak at the other two users. It means that, a communication link between the base station and User 1 is established, with User 2 and User 3 excluded. Similar results are shown in Figs. 19 and 20 when the base station communicates with User 2 and User 3, respectively, both of which demonstrate space division multiple access phenomena. That is, when there is a link between base station and User 2, the other two users receive little signal; and similar phenomenon holds true for User 3. In Figs. 18–20, results from ideal time-reversal and practical time-reversal always match each other very well. Therefore, it is concluded that the time-reversal system proposed in Section II could realize space division multiple access in practical time-reversal wireless communication.

The last example in this section is related to iterative time-reversal radar detection [4]. The problem geometry is given in Fig. 21. In the free space, there is one radar and two targets. Both targets are of cubic shape and made of a perfect conductor. The cube on the left has side length 0.03 m and its center is located at (0.135 m, 0.915 m, 0); the cube on the right is larger (side length 0.045 m) and its center is at (0.4575 m, 0.9225 m, 0). The radar consists of 13 antenna elements. As in the previous example,

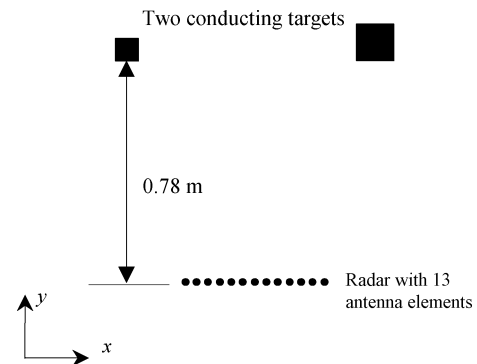


Fig. 21. Geometry of the radar detection example.

each antenna behaves as a z -direction oriented Hertzian dipole. The position of the left-most element is (0.21 m, 0.12 m, 0); and distance among elements is 0.015 m. To start the radar detection, the center element transmits periodic impulses to illuminate the two targets. The transmission is excited by a current source as in (15). The fields scattered by the two targets are then received by all 13 elements in the radar. Time-reversal is carried out at all the elements; and the reversed signals are radiated by all the antennas elements. The radiations from the 13 elements are focused onto the two targets. Since the two targets have different sizes, stronger fields are expected at the larger target. The above process could be repeated recursively. That is, the fields are scattered by the two targets; the scattered signals are received by the radar; the received fields are time-reversed and radiated to illuminate the targets again. According to [4], the contrast between the two targets would get larger and larger with more and more iterations. Such a phenomenon is also observed here. In Fig. 22, field strength along $y = 0.9$ m is plotted after the first, second, and third iterations. Specifically, field strength is represented by the maximum $|E_z|$ in the time history at a certain location. Also in Fig. 22, the field strength is normalized by the maximum value among all the locations along $y = 0.9$ m. As in the previous example, three sets of data are compared with one another.

- 1) Results from FDTD solver with ideal time-reversal.
- 2) Results from MoM solver with ideal time-reversal.
- 3) Results from FDTD solver with practical time-reversal.

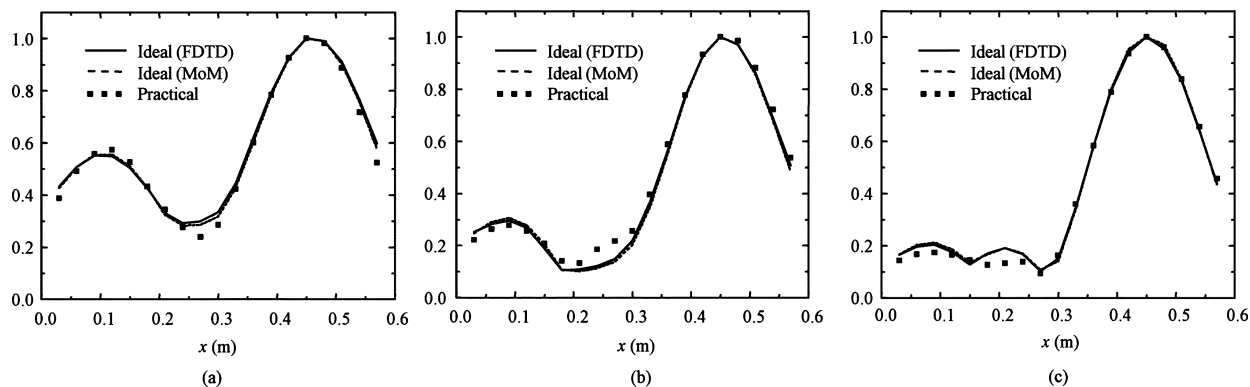


Fig. 22. Radar detection results at $y = 0.9$ m. (Vertical axes are normalized maximum $|E_z|$.) (a) After the first iteration. (b) After the second iteration. (c) After the third iteration.

The three sets of data agree with one another, and they all clearly show the iterative focusing phenomena: after the first iteration, there are two focusing points; and, with more and more iterations, the left focal point gets weaker and weaker. It means that the time-reversal circuit system proposed in this paper can be applied to practical radar scenarios.

IV. CONCLUSION

In this paper, a low-cost electronic circuit system is designed to time reverse short UWB impulses. The proposed system obtains discrete spectra of input impulses first; then achieves time-reversal in the frequency domain; and finally synthesizes time-reversed impulses using discrete continuous wave elements. This architecture is composed of commercially available semiconductor circuits, and hence, embodies a system-on-chip implementation of real-time time-reversal. Circuit-electromagnetic co-simulations using ADS and full-wave Maxwell's equations solvers demonstrate that the proposed system reliably accomplishes time-reversal of UWB impulses at the 3–10-GHz frequency band in the context of realistic electromagnetic wave propagation environments. On-board prototyping of the proposed system is currently ongoing. A major limitation of our present system is that the number of local oscillators is strictly dictated by the temporal duration of impulses. Schemes of using frequency sweeping to relieve this limitation are under investigation.

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